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APPLICATION NO.	FILING DATE		the state of the s	
		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/574,647	05/18/2000	Schelto van Doorn	00 P 7629 US 8087	
26161 7590 06/04/2004 FISH & RICHARDSON PC 225 FRANKLIN ST			EXAMINER LEON, EDWIN A	
BOSTON, M	IA 02110		ART UNIT	PAPER NUMBER
			2833	
			DATE MAILED: 06/04/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary Examiner		Application No.	Applicant(s)	<u>~</u>			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. Edensions of time may be available under the provisions of 3 TGR 1.136(s). In no event, however, may a reply be timely filled after 5X (6) MONTHS from the mailing date of this communication. If the period for reply specified above, the maximum statutory period will apply and will expire 5X (6) MONTHS from the mailing date of this communication. If the period for reply specified above, the maximum statutory period will apply and will expire 5X (6) MONTHS from the mailing date of this communication. Failure to reply within the scried above, the maximum statutory period will apply and will expire 5X (6) MONTHS from the mailing date of this communication and period will apply and will expire 5X (6) MONTHS from the mailing date of this communication and period will apply and will expire 5X (6) MONTHS from the mailing date of this communication and period will apply and will expire 5X (6) MONTHS from the mailing date of this communication. Any reply received by the Office later than there months call the statutory and period will apply and will expire 5X (6) MONTHS from the mailing date of this communication. **Status** 1) □ Responsive to communication(s) filled on \$\frac{19 February 2004}{2}\$. 2a) □ This action is FINAL. 2b) □ This action is non-final. 3) □ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under \$Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims* 4) □ Claim(s) \$\frac{1.19}{1.9!} \text{ s/are allowed.} 5) □ Claim(s) \$\frac{1.9!}{1.9!} \text{ s/are allowed.} 6) □ Claim(s) \$\frac{1.9!}{1.9!} \text{ s/are allowed.} 9) □ The specification is objected to by the Examiner. Application Papers 9) □ The drawing(s) filed on \$\frac{1.5}{1.9!} s/	Office Action Summary		VAN DOORN, SCHELTO				
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Attachment(s)	Attachment(s)		,				
1) Notice of References Cite of (PTO 200)		٠					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date.	2) U Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Da	te ´,				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date S. Patent and Trademark Office 5) Notice of Informal Patent Application (PTO-152) 6) Other:	3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	5) Notice of Informal Pa	atent Application (PTO-152)				

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DETAILED ACTION

Response to Amendment

1. Applicant's Response filed February 19, 2004 has been place of record in the file.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-4 and 6-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Curzio (U.S. Patent No. 5,611,013). With regard to Claim 1, Curzio discloses a transducer comprising a housing (1) mountable on a substrate (3), the housing (1) configured to receive a jumper cable (5, 7, 9), an input/output (I/O) lead (conductors inside 39, 45, Column 7, Lines 18-35) supported by the housing (1) and configured to contact an I/O lead (43) of an integrated circuit (37) mounted on the substrate (3) and electronic circuitry (inside 1) supported by the housing (1) to transition between an electronic data transfer protocol of the jumper cable (5, 7, 9) and an electronic data protocol of the integrated circuit (37). See Figs. 1-2 and 6.

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With regard to Claim 2, Curzio discloses the transducer I/O lead (conductors inside 39, 45, Column 7, Lines 18-35) being configured to electrically connect to the integrated circuit (37) I/O lead (43) independently of any electrically conductive path of the substrate (3). See Figs. 1-2 and 6.

With regard to Claim 3, Curzio discloses the transducer I/O lead (conductors inside 39, 45, Column 7, Lines 18-35) being a configured to contact the integrated circuit (37) I/O lead (43) at a transducer surface substantially parallel to a mounting surface (upper surface of 3) of the substrate (3). See Figs. 1-2 and 6.

With regard to Claim 4, Curzio discloses the transducer I/O lead (conductors inside 39, 45, Column 7, Lines 18-35) being configured to contact a pin I/O lead (43) of the integrated circuit (37). It is the Examiner's opinion that lead (43) can be considered a pin lead since Applicant's lead has a curved shape but it still being considered a pin lead. See Figs. 1-2 and 6.

With regard to Claim 6, Curzio discloses the transducer I/O lead (conductors inside 39, 45, Column 7, Lines 18-35) being configured to contact the integrated circuit (37) I/O lead (43) at a transducer surface adjacent to a mounting surface (upper surface of 3) of the substrate (3). See Figs. 1-2 and 6.

With regard to Claim 7, Curzio discloses a power input lead (43) connectable to a power line (13) of the substrate (3). See Figs. 1-2 and 6.

With regard to Claim 8, Curzio discloses a transductional device (15). See Figs. 1-2 and 6 and Column 5, Lines 60-66.

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With regard to Claim 9, Curzio discloses the transductional device (15) being an opto-electronic device (15). See Figs. 1-2 and 6 and Column 5, Lines 60-66.

With regard to Claim 10, Curzio discloses the transductional device (15) being an electronic device (15). Since the opto-electronic component (15) has some electronic circuits as mentioned in Column 5, Line 66, it is the Examiner's opinion that opto-electronic component (15) can be considered an electronic device. See Figs. 1-2 and 6 and Column 5, Lines 60-66.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claim 5 rejected under 35 U.S.C. 103(a) as being unpatentable over Curzio (U.S. Patent No. 5,611,013) in view of Lemke et al. (U.S. Patent No. 6,024,584). Curzio discloses the claimed invention except for the transducer I/O lead being configured to contact a solder ball lead of the integrated circuit.

Lemke et al. discloses the use of solder balls leads (100) to connect an electrical connector (10) and a circuit board (204). See Figs. 5-6.

Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the transducer of Curzio by changing the lead from the

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integrated circuit to a solder ball lead as taught in Lemke et al. in order to provide a more reliable electrical path between the transducer and the integrated circuit.

Response to Arguments

6. Applicant's arguments filed February 19, 2004 have been fully considered but they are not persuasive. In response to Applicant's arguments regarding Claim 1, that the Curzio reference does not show the lead supported by the housing being directly contacting the lead for the integrated circuit mounted in the substrate, Applicant's attention is directed to Fig. 6 and Column 7, Lines 18-35 in which Curzio clearly discloses input/output (I/O) lead (conductors inside 39, 45, Column 7, Lines 18-35) supported by the housing (1) and configured to contact an I/O lead (43) of an integrated circuit (37) mounted on the substrate (3).

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edwin A. León whose telephone number is (571) 272-2008. The examiner can normally be reached on Monday - Friday 10:00-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on 571-272-2800, extension 33. The fax

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phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Gary Paumen Primary Examiner

Edwin A. Leon AU 2833

EAL May 31, 2004